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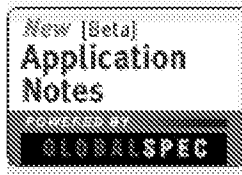
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Garrett, D.; Stan, M.;
[Low Power Electronics and Design, 1997. Proceedings., 1997 International Symposium on](#)
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2. Low-power data-dependent 8/spl times/8 DCT/IDCT for video compression
Pai, C.-Y.; Lynch, W.E.; Al-Khalili, A.J.;
[Vision, Image and Signal Processing, IEE Proceedings -](#)
Volume 150, Issue 4, 22 Aug. 2003 Page(s):245 - 255
Digital Object Identifier 10.1049/ip-vis:20030564

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3. Low Power Arithmetic Units for Video Processing Systems
Ngo, H.T.; Asari, V.K.;
[Communication systems, 2006. ICCS 2006. 10th IEEE Singapore International Conference on](#)
Oct. 2006 Page(s):1 - 5
Digital Object Identifier 10.1109/ICCS.2006.301397

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4. An 8.5-ns 112-b transmission gate adder with a conflict-free bypass circuit
Sato, T.; Sakate, M.; Okada, H.; Sukemura, T.; Goto, G.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 27, Issue 4, April 1992 Page(s):657 - 659
Digital Object Identifier 10.1109/4.126557

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5. The Parity protected, multithreaded register files on the 90-nm itanium microprocessor
Fetzer, E.S.; Dahle, D.; Little, C.; Safford, K.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 41, Issue 1, Jan. 2006 Page(s):246 - 255

Digital Object Identifier 10.1109/JSSC.2005.859884

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6. Low power parallel multiplier with column bypassing
 Ming-Chen Wen; Syng-Jyan Wang; Yen-Nan Lin;
[Circuits and Systems, 2005. ISCAS 2005. IEEE International Symposium on](#)
 23-26 May 2005 Page(s):1638 - 1641 Vol. 2
 Digital Object Identifier 10.1109/ISCAS.2005.1464918

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7. 66 M/70 mW HS and ultra-low power 16x16 MAC design using TG for web-based multimedia
 Seung-Min Lee; Jin-Hong Chung; Hyung-Seok Yoon; Lee, M.M.-O.;
[ASICs, 1999. AP-ASIC '99. The First IEEE Asia Pacific Conference on](#)
 23-25 Aug. 1999 Page(s):151 - 153
 Digital Object Identifier 10.1109/APASIC.1999.824050

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8. Computational delays and habits
 Bugmann, G.;
[Self-Learning Robots III: Brainstyle Robotics: The Cerebellum Beyond Function Approximation \(R](#)
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